
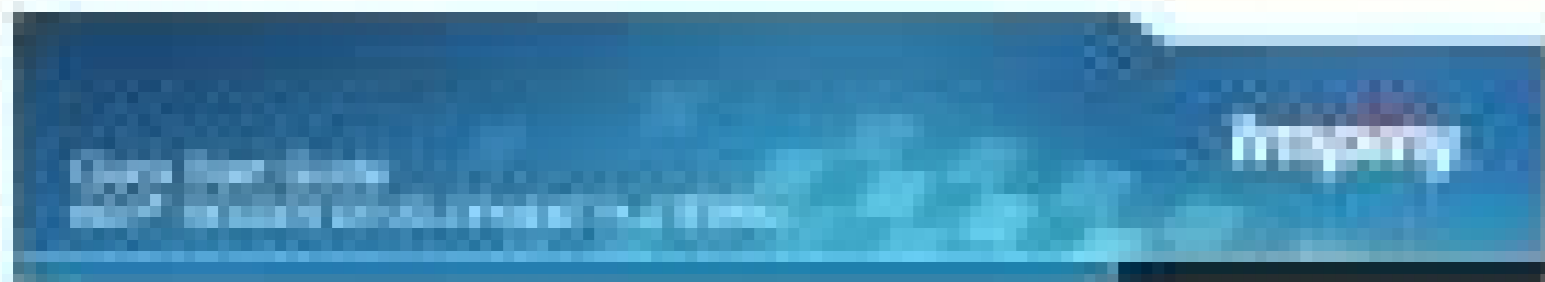


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Impinj's R2000 is a high-performance UHF RFID reader designed for industrial and retail applications. It features a rugged design and is available in both rack-mountable and desktop configurations.

Key Features:

- 1000 WPM (Watts Per Meter) Development Platform
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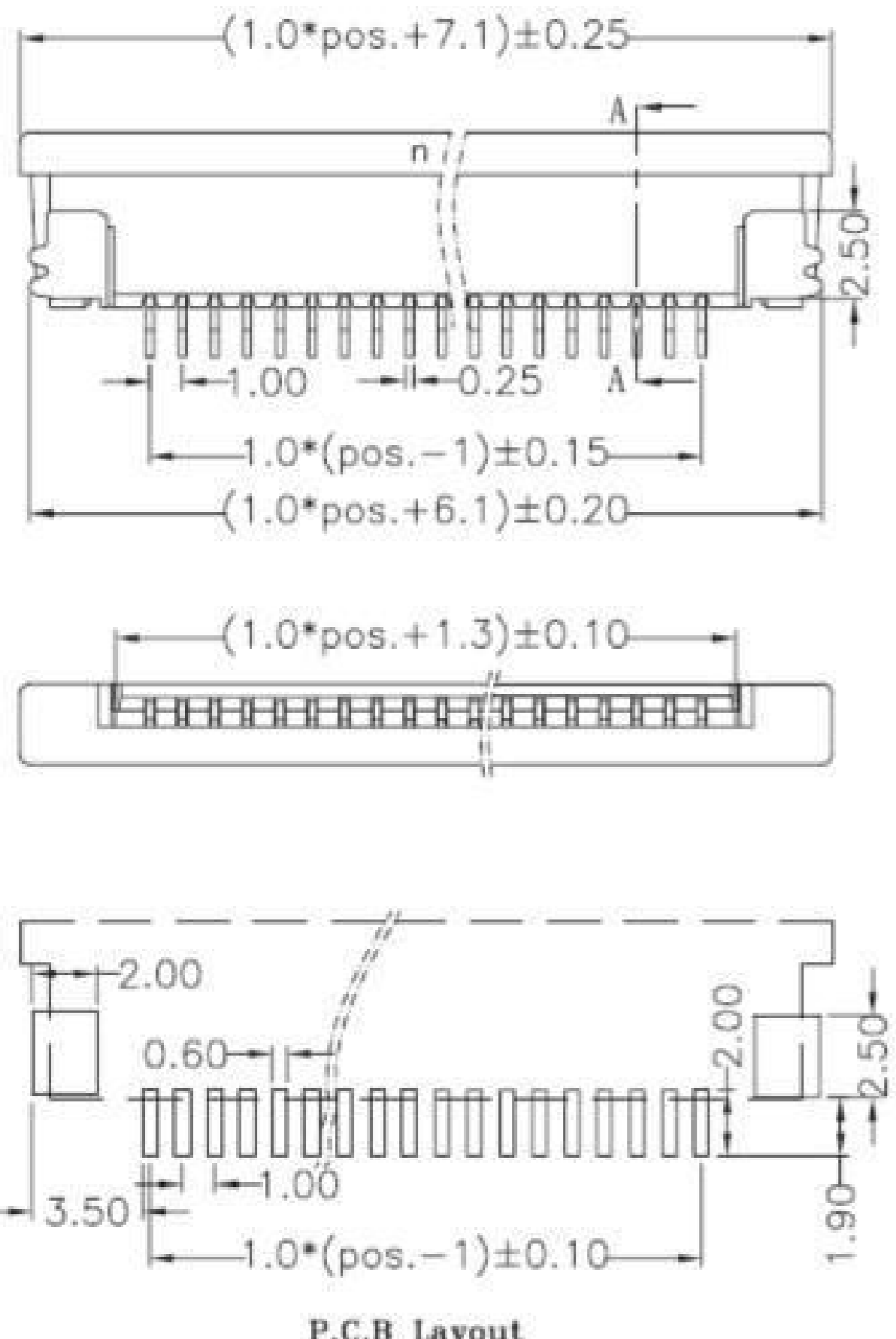
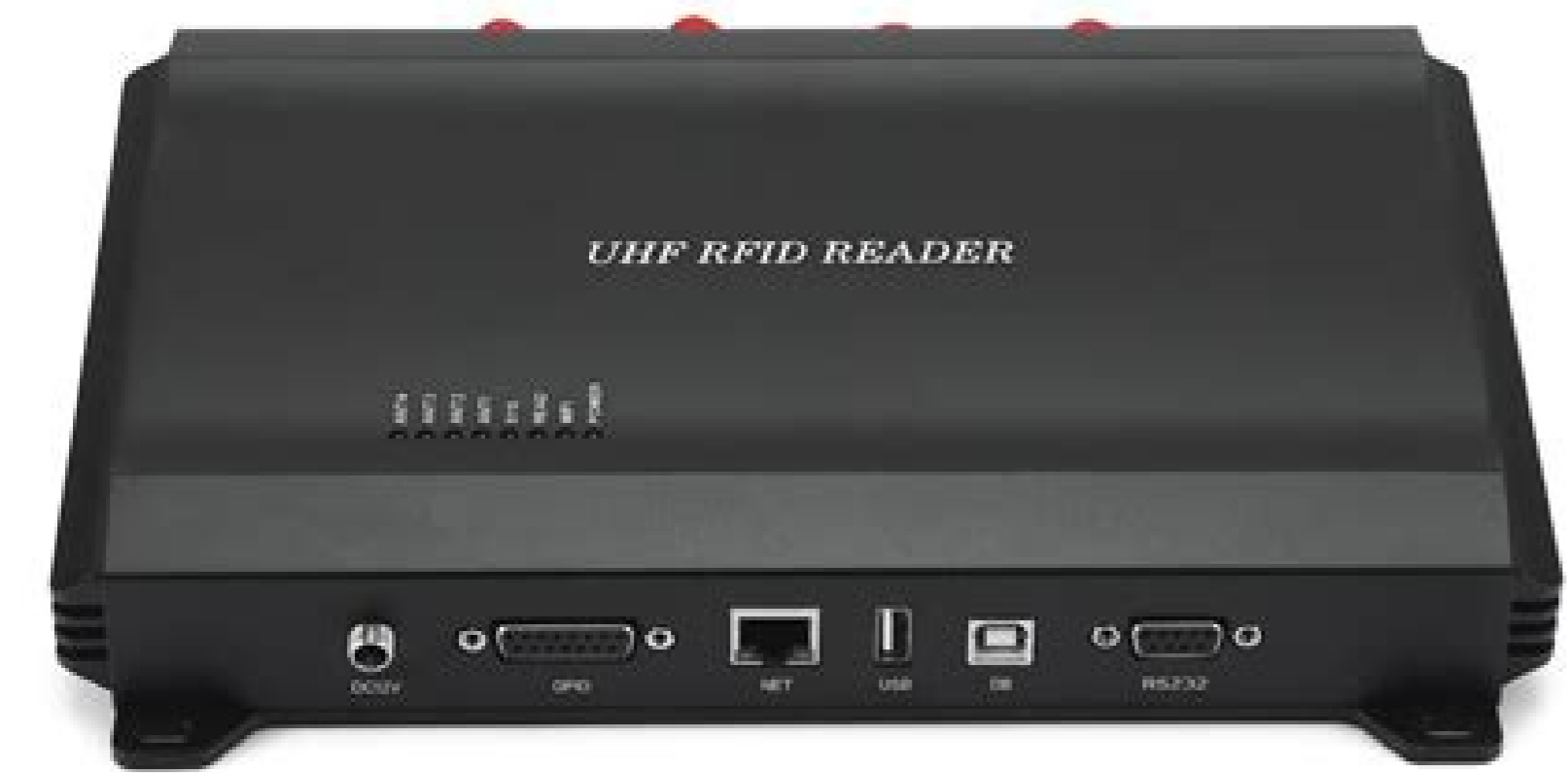


Reading Tags with the Body Development Platform

1. Connect the R2000 to a PC using the provided USB cable.
2. Install the Impinj R2000 software on the PC.
3. Configure the software to match the reader's settings.
4. Place the reader in a location with a clear line of sight to the tags.
5. Run the software to begin reading tags.

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Impinj, Inc. 1000 WPM (Watts Per Meter) Development Platform



P.C.B Layout



Impinj r2000 datasheet pdf. Impinj indy r2000 datasheet.

4 3 PIN LISTING/SIGNAL DEFINITIONS 4 FIGURE 2: INDY R2000 READER CHIP SAMPLE APPLICATION 1 1.2 REFERENCE DOCUMENTS 20

5.4 TRANSMITTER MODES DAC TESTING Please email us at DIGITAL-SECURITY@avnet.com, using Reference Number: 18.21f01502.1649915903.af61c4b Thank you in advance for your assistance in resolving this issue. 59 8.1 CARRIER SETTling TIME 18 5.2 ANTENNA CONFIGURATION SCENARIOS 21 6 FREQUENCY GENERATION

14 5.1.3 Receive RF Interface 25 7.1 SERIAL INTERFACE 14 FIGURE 4: RECEIVER FRONT-END EXTERNAL OUTPUT INTERFACE, INCLUDING DRM LC LOAD OF MIXER 21 5.4.2 Low Power Non-linear Mode (DSB-ASK) 70 9 PACKAGE NFO mm 58.0 x mm 9 x mm 9 niP 46 otehcCaP zHM 069048 elatigid esab adnab a esab id ezneugerF elibarugifnoC medoM BD 52 >a elibaloger aticU id aznetoP inoizatserp etla da inoizacilppa rep otatroppus onretse aznetop id erotacifilpmA .mhd 71 a onif elibarugifnoc otargetni aznetop id erotacifilpmA ydnl ontemirefir id ngiseD ad otatroppus noN 70.1enoisrev X-PI e OCIPi ,B6-00081 OSI)MRD(edoM redaeR esneD sedoM sedoM noitaludoM timsnarT ksA-RP e BSS ,BD C6-00081 OSI / 2 neG 1 essaC FHU LABOLGCPE LOCOTORP E cafretnl riA acimaronA pihC redaeR 0002R ydnl)0002RJPI (pihC erottel 0002R ydnl acimret e acinacem ,acirttele acificeps 20-70-2102 3.1 veR Jottetorp liamEJ erattatnoc ,inoizamrofni iroiretlu reP enoizudortnl 1 oirammoS III aznetop id ontemaveliR FR 3.5 91 esab adnab a iccafretni iveciR 5.1.5 51 lortnoC rewoP latigid 5.8 86 ,erottel led itnemges i ittut azziridni redaeR ydnl id pihC id allgimaf al ,elibinopsid otla 'Aip redaeR id erottel id pihC la otsoC la libisnes 'Aip inoizacilppa el rep inoizatserp enoub ednerpmoc ehC inoizatserp / ozzerp id ollavretni nU noc E Atilanoizunuf id angescoc al etnarud erottel led ingesid led Atisselpmoc al onocudir aigolonimreT 1.1 1 of low power consumption power, from 1100 to 880 mW dependent configuration: 200 W Standby. Standby, enoizallecnae id occolB 4.1.5 51 ,enoizudorp alled e ngised led otsoC li onocudir ydnl erottel led pihC i ,inedecerp aznes erotzaitetnis esaf id eromur :31 arugif 32 annetna aipod a oiranecs :01 arugif 02 jideihC BSD(onretse AP noc 0002R ydnl erottel id pihC id enoissimsart :11 arugif 02 ,alacs alla avitaler remmaj-otua id enoizallecnae al opod aciroet aticU :7 arugif 61 olognis annetna oiranecs :9 arugif 81 ,amelborp led ic acifiton allen otua out li omaideihC e etnemaires otlom inoitseuq etseuq omaidnerP ... remmaj-fles mhd 51+ eritseq rep otarugifnoc dne-tnorf onretse ossergni id aiccafretnl erotiveciR :3 arugif 5 ,cni ,jnipml ,2102 thgiryoc ,3.1 enoisveR acimret acificeps E ocinacem ,ocirttele 0002R ydnl moc ,jnipml.troppsU :a jnipml otropps id elatrop li eratisiv ,ocincet otropps rep erotacifilpmA rewoP Xtecafretnl erotacifilpmA relortnocorcIM pihC remedeG DIFR2 NEG FHU ydnl)1.4.1V(1-802 203 TI ISTE id otuges A inoiger ERTLA E APORUE 51 ETRAP 1 ,HC GFC 74 CCF SU euges ehC inoiger ertla e adanaC ,ititU itatS snoigerR troppsU reirraC MBD01 + noC)MRD(MBD48-)MRD(MBD 59- ;MBD 011- adnab id azzehgral id elibarugifnoc Atilibisnes ,gat rep ISSR SOMCIB egiS m 81.0 69 8.6 Test ADC ADC 6 4 ELECTRICAL SPECIFICATIONS 15 FIGURE 6: SELF-JAMMER CANCELLATION ARCHITECTURE 8 4.1 ABSOLUTE MAXIMUM RATINGS

69 8.7 AUX. 14 FIGURE 5: LOCAL OSCILLATOR INPUT EXTERNAL INTERFACES These highly integrated, high-performance reader chips and supporting software deliver the key building blocks for a wide spectrum of UHF RFID readers. 17 Impinj and Indy are either registered trademarks or trademarks of Impinj, Inc. 14 5.1.1 Receiver Front-end Circuitry

FIGURE 8: SELF-JAMMER CANCELLATION GRID AND CALIBRATION EXAMPLES. 21 5.4.1 Full Power Non-linear Mode (DSB-ASK) 23 7 DEVICE CONTROL AND PROGRAMMING 14 5.1.2 Local Oscillator Input Click Here To Download: eAAeABrochure: Impinj IndyA@A Reader Chip Family eAAeADatasheet: IndyA@A R1000 Reader Chip eAAeADatasheet: IndyA@A R2000 Reader Chip The Impinj UHF RFID product line includes the Indy reader chip family, based on award winning technology acquired from Intel Corporation. 8 4.3 TRANSCIEVER FUNCTIONAL enoissimsart id aticU id elarttpeS tseT 3.8 06 elanges led inoizinfid e ocnelE niP 1.3 6 eraenil AtiladoM 3.4.5 12 appaM itartsiger 2.7 72

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Description; Specifications; Part Numbers; The 1128 Bluetooth® UHF RFID Reader is designed to read and write to EPC Class 1 Gen 2 (ISO18000-6C) UHF transponders and communicate with a variety of host devices via Bluetooth® wireless technology. With its Impinj R2000 core and range of interchangeable high performance antennas the 1128 performs like no other reader, giving ...

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